

Model Name: P35-DS3R Rev1.01

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03	BLOCK DIAGRAM
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24	PCI EXPRESS*1 ,PCI SLOT 1,2
25	ITE8718/GB,RESET DRIVE
26	COM,LPT
27	BIOS,CI,HWM,KB/MS

SHEET TITLE

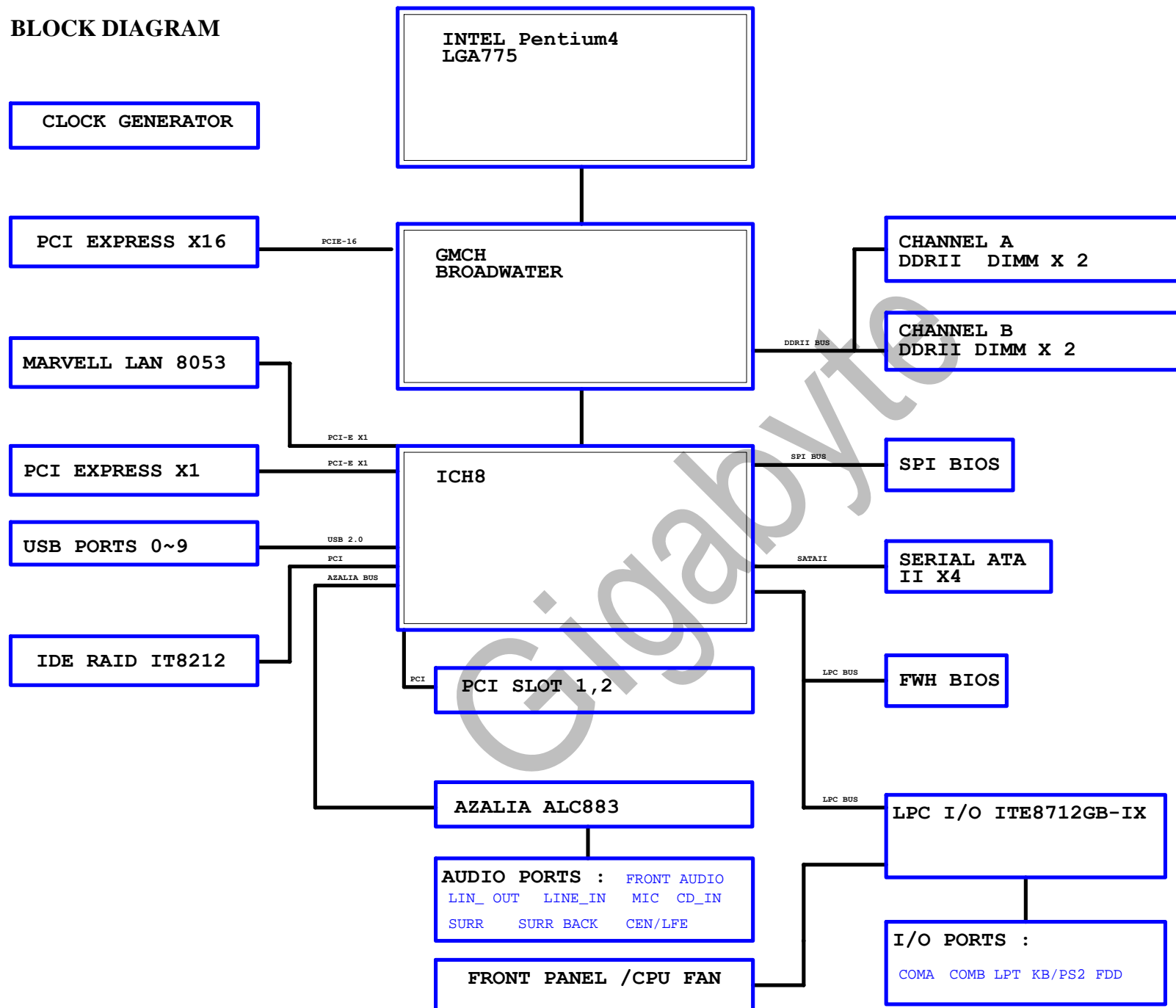
28	AZALIA ALC889A
29	AUDIO JACK
30	VCORE PWM ISL6327
31	DISCRETE POWER
32	ATX POWER
33	JMicron JMB363
34	LAN REALTEK RTL8111B
35	FRONT PANEL,FUSB,FDD

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BOM & PCB MODIFY HISTORY			
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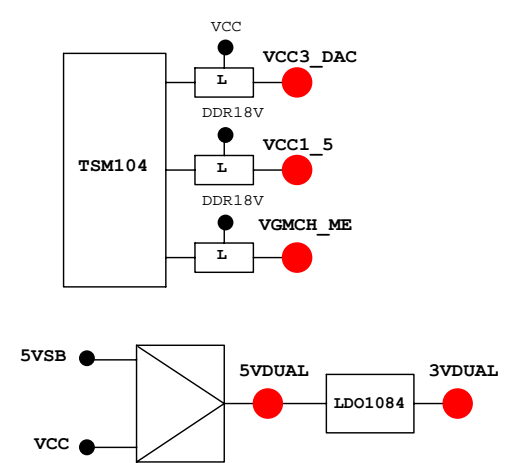
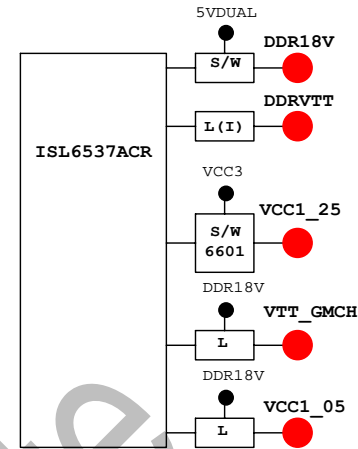
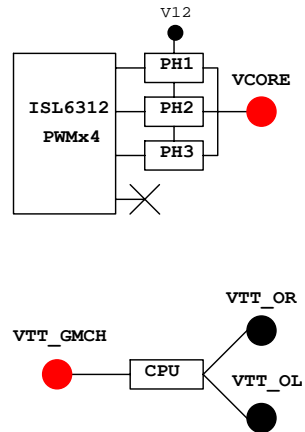
BLOCK DIAGRAM



ICH8 GPIO LIST TABLE

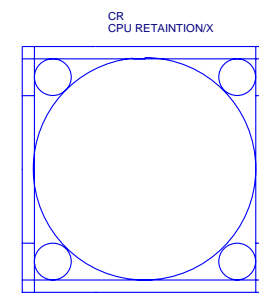
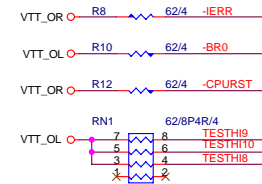
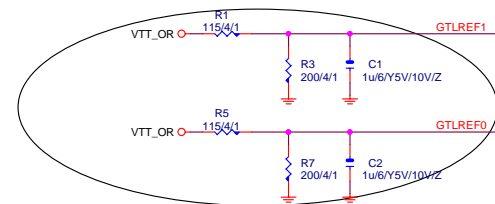
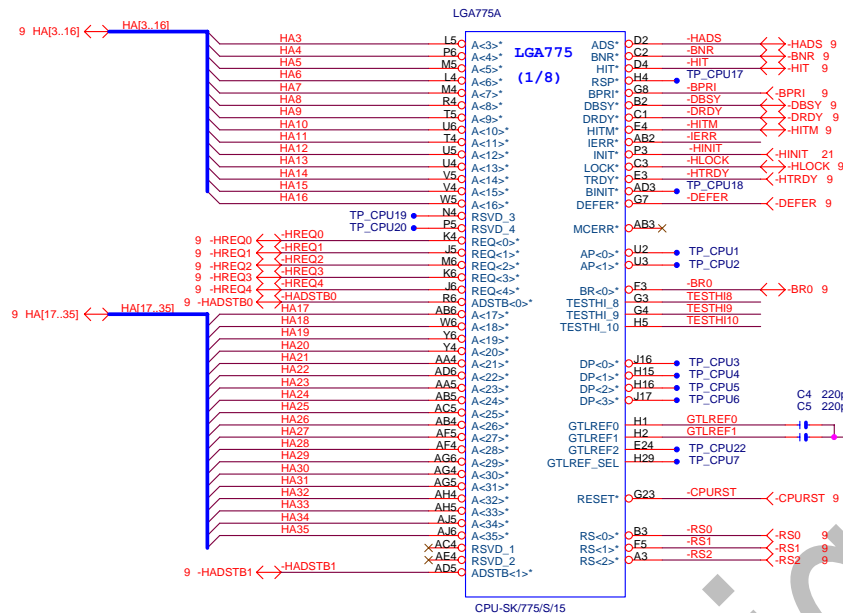
PIN NAME	PWR WELL	AFTER/ PLTRST	USAGE	NOTE
GP0	MAIN	IN	-ACZ_DET	P/U 8.2K VCC3
GP1/TACH1	MAIN	IN	ICH_FAN_TACH1	P/U 8.2K VCC3
GP2/PIRQE#	MAIN	IN	-PIRQE	P/U 8.2K VCC3
GP3/PIRQF#	MAIN	IN	-PIRQF	P/U 8.2K VCC3
GP4/PIRQG#	MAIN	IN	-PIRQG	P/U 8.2K VCC3
GP5/PIRQH#	MAIN	IN	-PIRQH	P/U 8.2K VCC3
GP6/TACH2	MAIN	IN	ICH_FAN_TACH2	P/U 8.2K VCC3
GP7/TACH3	MAIN	IN	ICH_FAN_TACH3	P/U 8.2K VCC3
GP8	STBY	IN	GPIO8(DUALBIOS_INPUT)	P/U 8.2K 3VDUAL
GP9	STBY	OUT	WOL_ONLY	P/D 100K GND
GP10	STBY	IN	CLGPIO1	P/U 8.2K 3VDUAL
GP11/SMBALERT#	STBY	OUT	-SMBALRT	P/U 8.2K 3VDUAL
GP12	STBY	IN	MB_ID0	P/U 8.2K 3VDUAL
GP13	STBY	IN	-LPCPME	P/U 8.2K 3VDUAL
GP14	STBY	IN	CLGPIO2	P/U 8.2K 3VDUAL
GP15	STBY	OUT	LAN_DISABLE(STP_PCI-)	N/A
GP16	MAIN	OUT/LOW	RESET	N/A
GP17/TACH0	MAIN	IN	ICH_FAN_TACH0	P/U 8.2K VCC3
GP18	MAIN	OUT	MB_ID1	P/U 8.2K VCC3
GP19	MAIN	IN	SATA1GP	P/U 8.2K VCC3
GP20	MAIN	OUT	-SPI_WP0	P/U 1K 3VCL
GP21	MAIN	IN	SATA0GP	P/U 8.2K VCC3
GP22	MAIN	IN	SCLOCK	P/U 8.2K VCC3
GP23	MAIN	OUT	-LDRQ1	P/U 8.2K VCC3
GP24	STBY	OUT	CLGPIO0	P/U 8.2K 3VDUAL
GP25	STBY	IN	MB_ID2(STP_CPU-)	P/U 8.2K 3VDUAL
GP26/S4_STATE#	STBY	OUT	S4_STATE#	P/U 8.2K 3VDUAL
GP27	STBY	OUT/LOW	GPIO27(EL_STATE0)	P/U 8.2K 3VDUAL
GP28	STBY	OUT/LOW	PWR_LED(EL_STATE1)	N/A
GP29/OC5#	STBY	IN	-USBOC_R	P/U FUSEVCC
GP30/OC6#	STBY	IN	-USBOC_R	P/U FUSEVCC
GP31/OC7#	STBY	IN	-USBOC_R	P/U FUSEVCC
GP32	MAIN	OUT	DUAL_BIOS	P/U 100K+1M VCC3
GP33	MAIN	OUT	DUAL_BIOS	P/U 8.2K VCC3
GP34	MAIN	OUT/LOW	GPIO34/SMB_RST	N/A
GP35	MAIN	OUT	SATACLKREQ#	N/A
GP36	MAIN	IN	SATA2GP	P/U 8.2K VCC3
GP37	MAIN	IN	SATA3GP	P/U 8.2K VCC3
GP38	MAIN	IN	SLOAD	P/U 8.2K VCC3
GP39	MAIN	IN	GPIO39	P/D 8.2K GND
GP48	MAIN	IN	GPIO48	P/U 8.2K VCC3
GP49	MAIN	IN	CPUPWROK	P/U 100 VTT_OL

VCORE:3 PHASE PWM--ISL6312

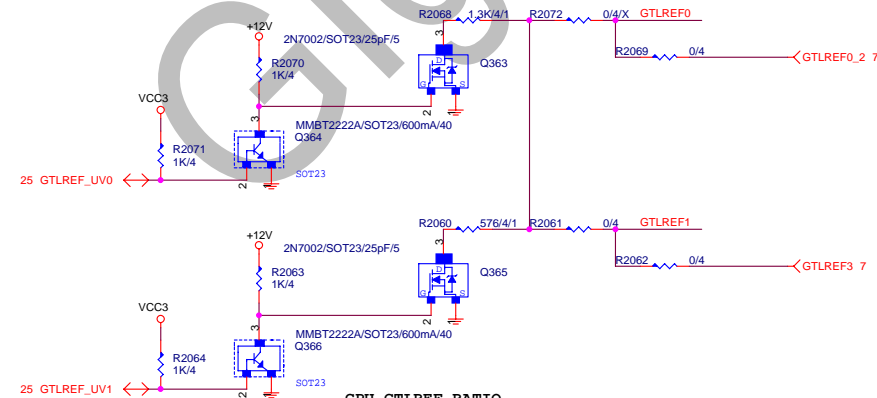
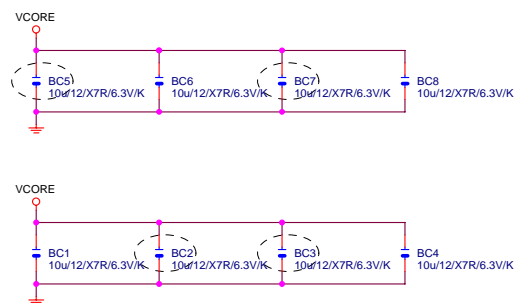


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Title		TABLE LIST	
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HA/REQ:4/11 50+-15%
ADSTB:4/14 50+-15%

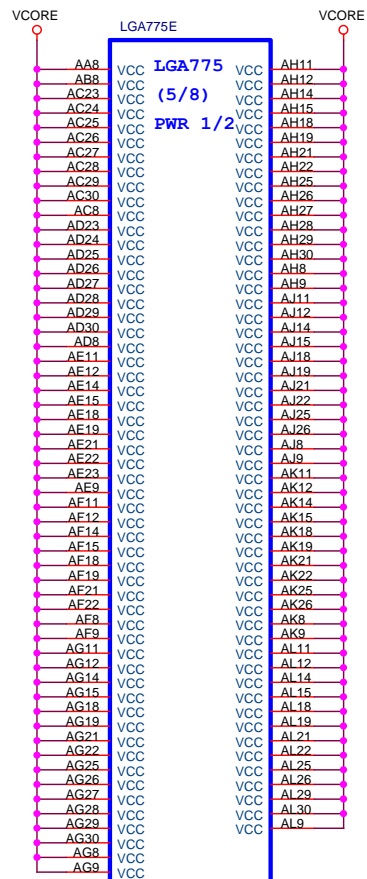


Impedance=50 +- 15% for 4 -layer

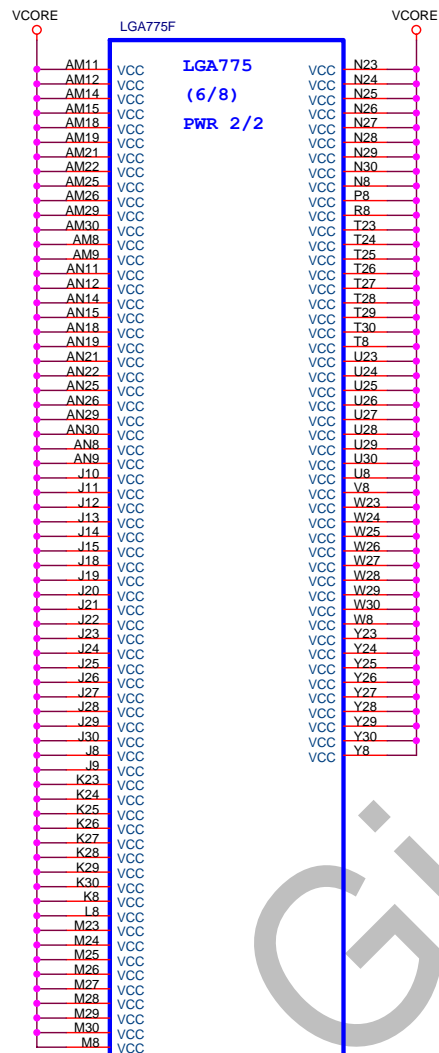


GTLREF_UV0	GTLREF_UV1	Ratio Set
HIGH	HIGH	0.67
LOW	HIGH	0.65
HIGH	LOW	0.63
LOW	LOW	0.615

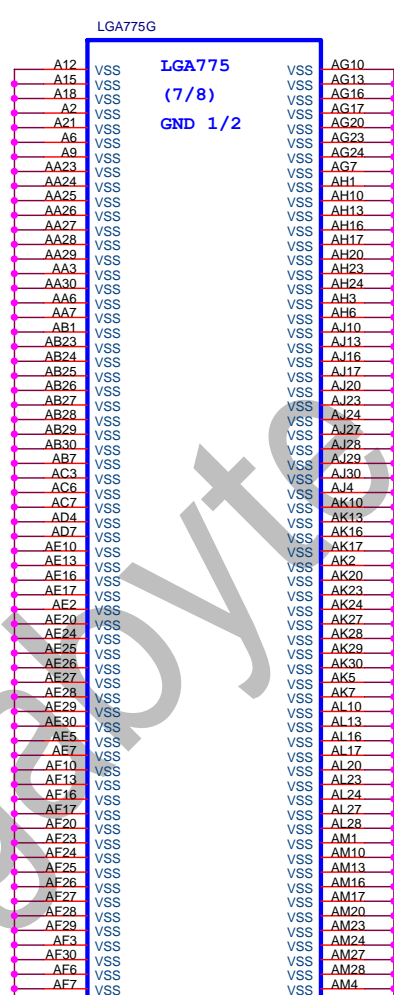
Gigabyte Technology			
Title			
P4_LGA775-A			
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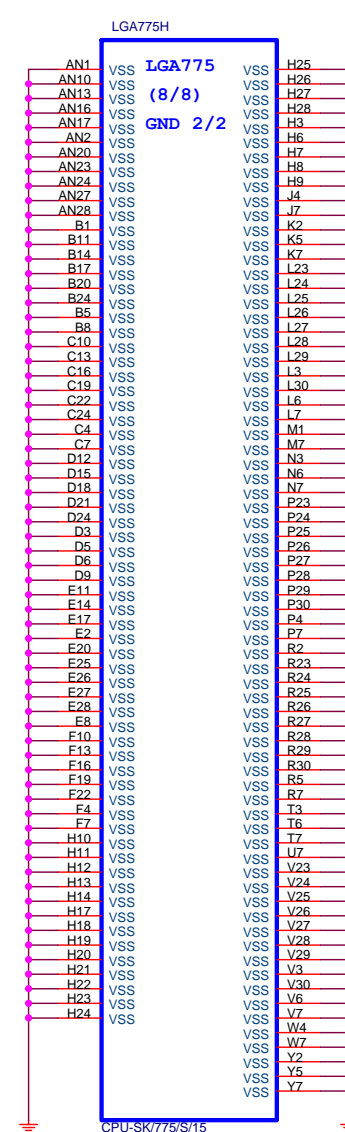
CPU-SK/775/S/15



CPU-SK/775/S/15



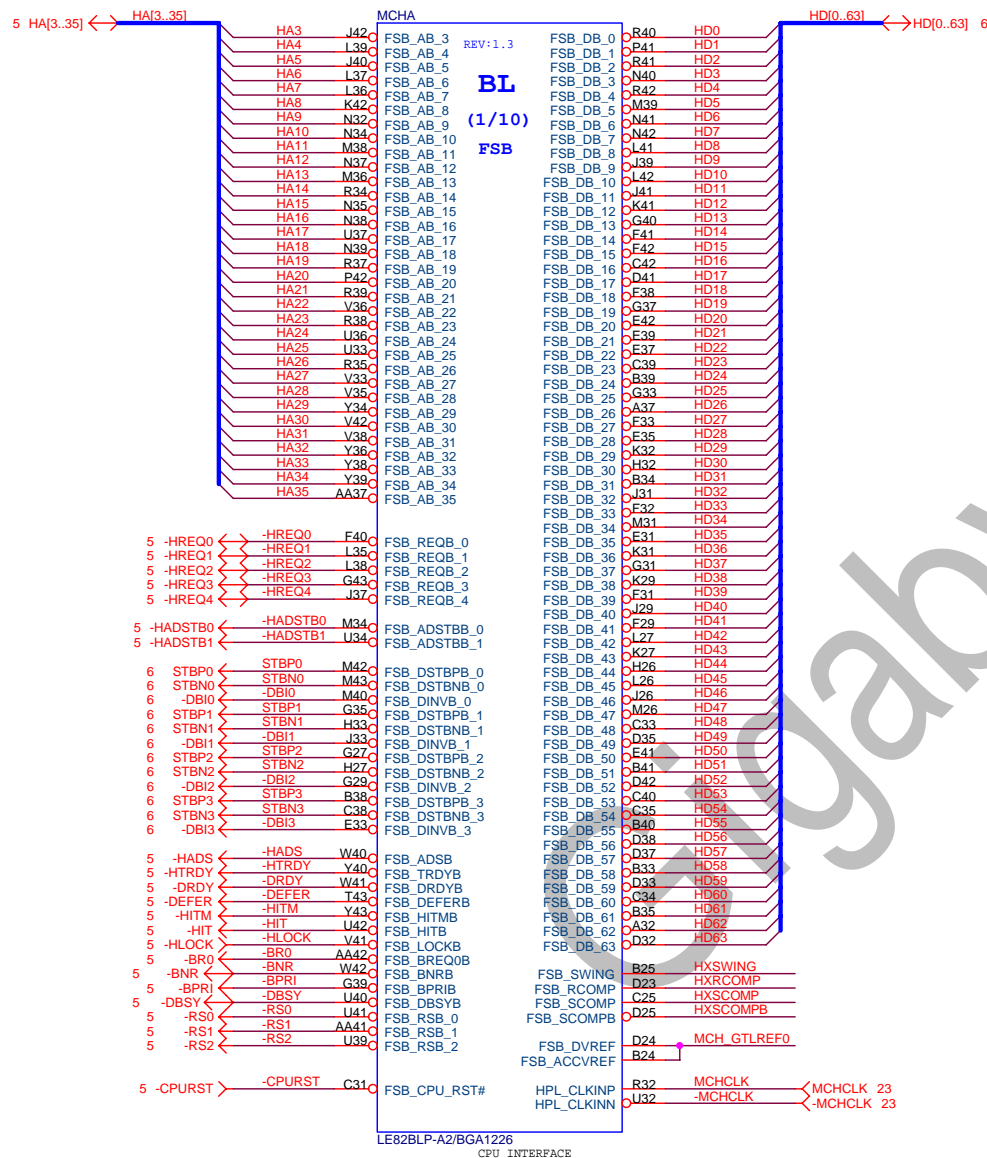
CPU-SK/775/S/15



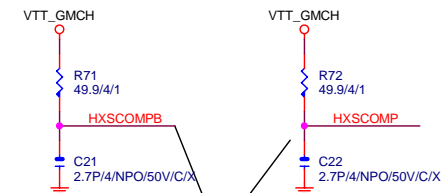
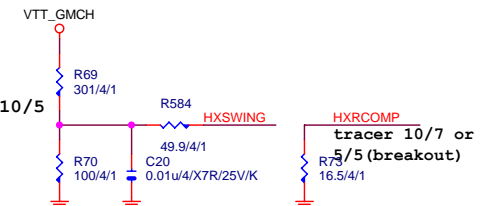
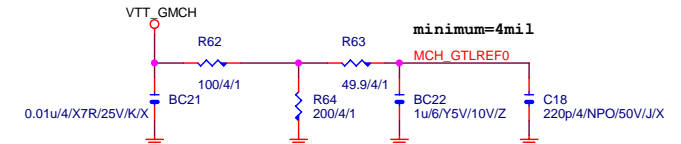
CPU-SK/775/S/15

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Title				P4_LGA775-E,F,G,H			
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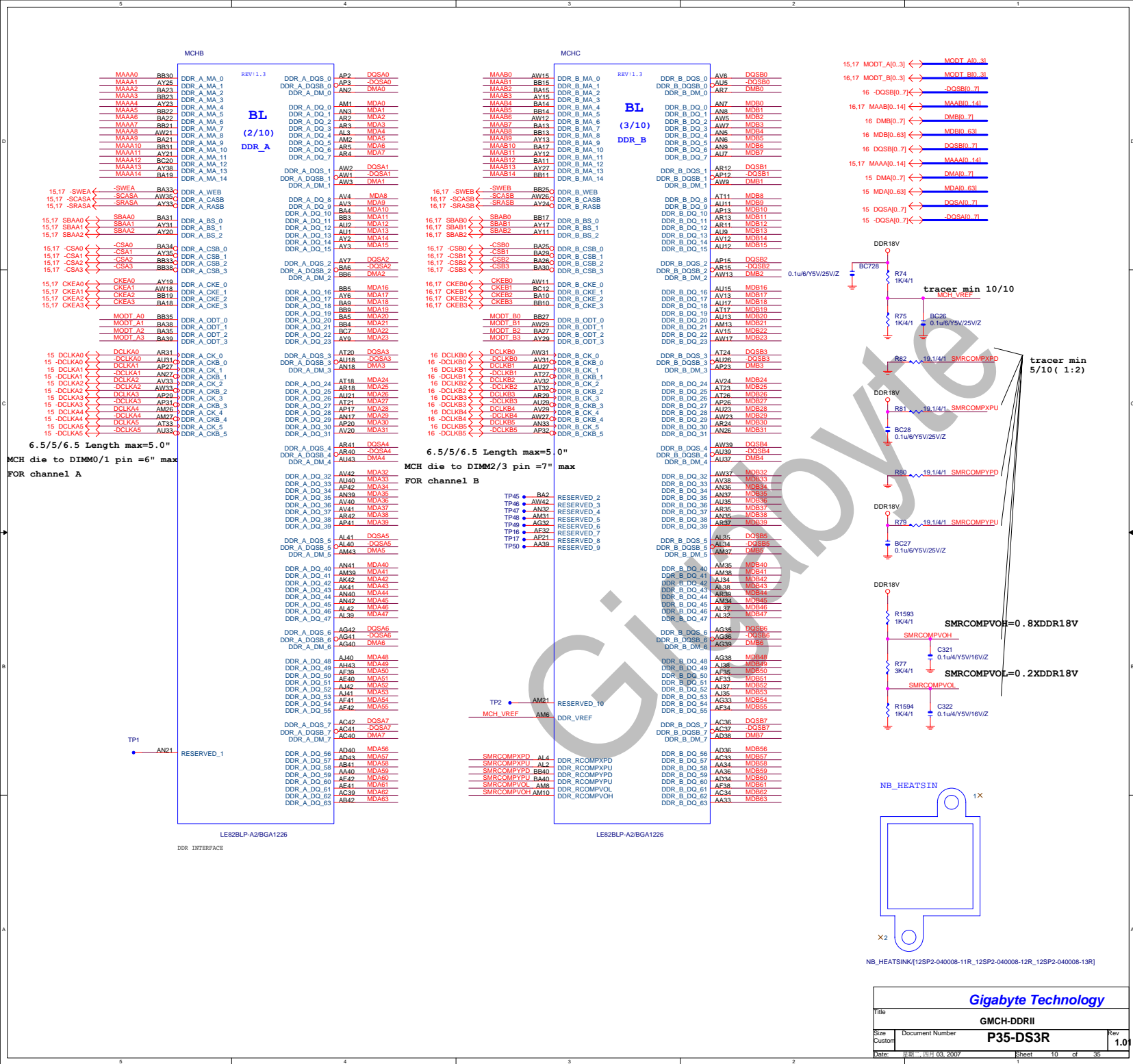


tracer min 10/10 or (10/5
breakout) ,L1<3"



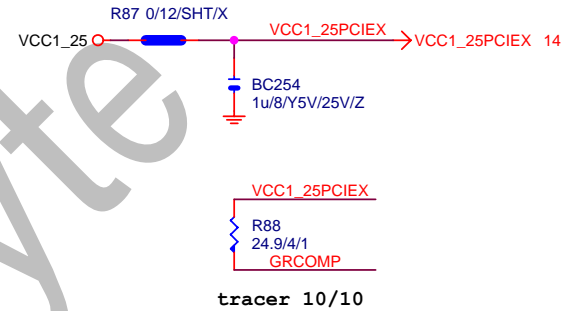
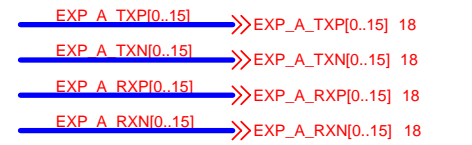
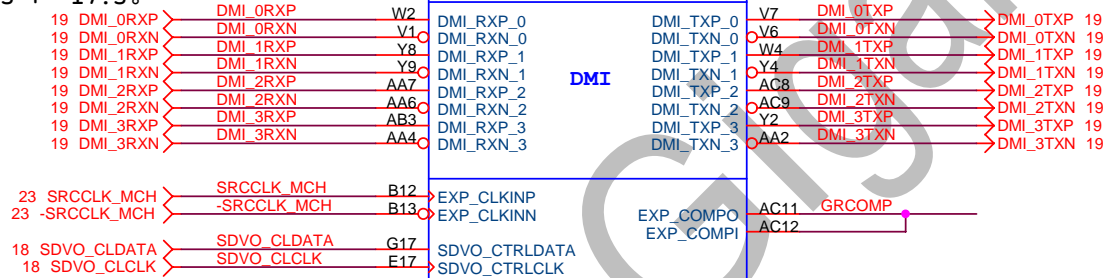
Gigabyte Technology

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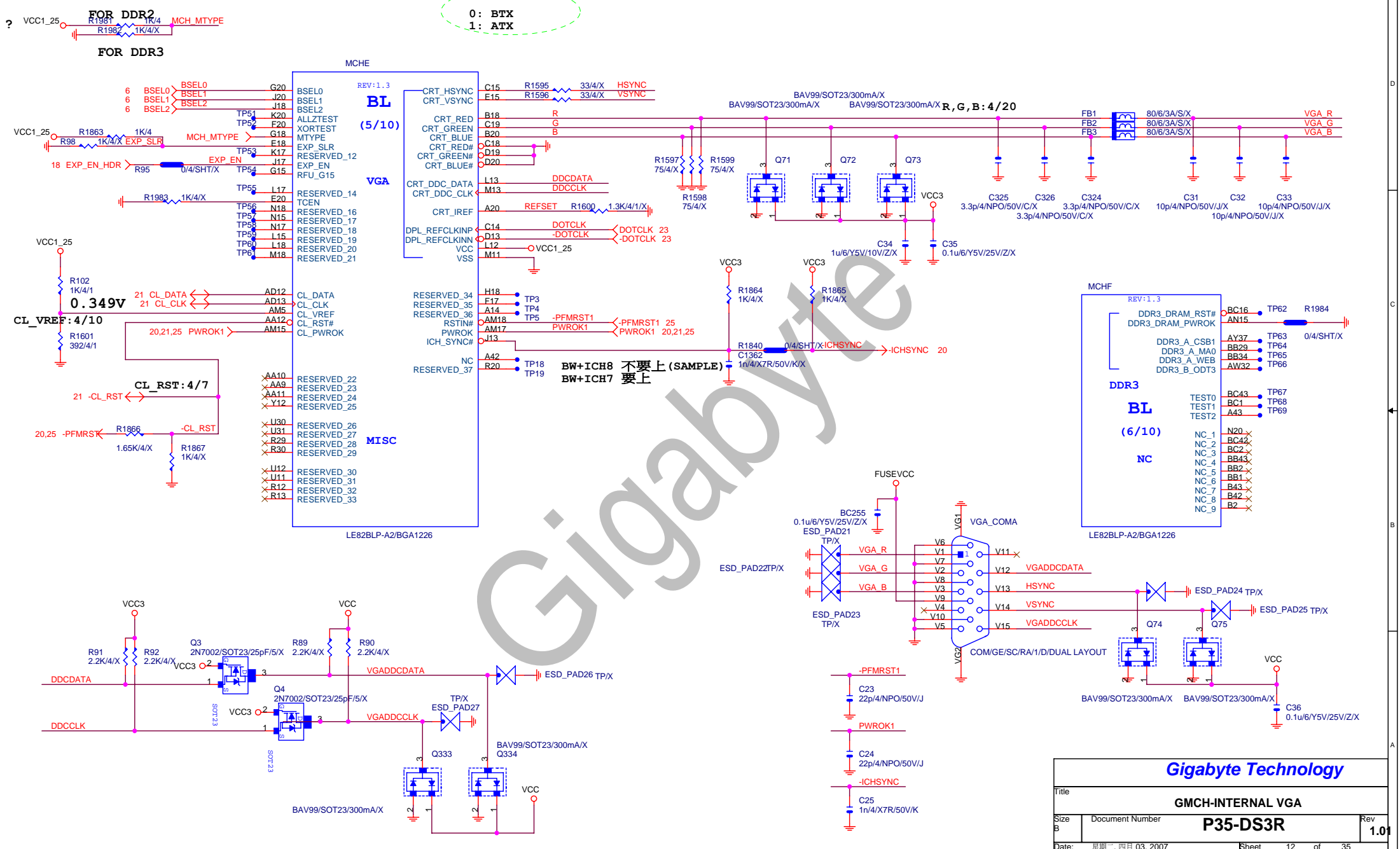
PCIEX16:15/4/8/4/15
Impedance=95 +- 17.5%

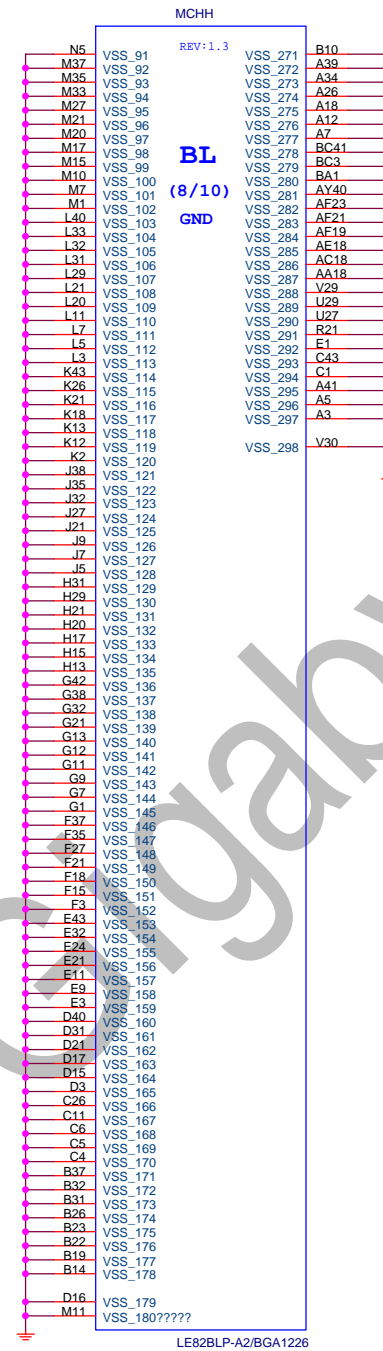
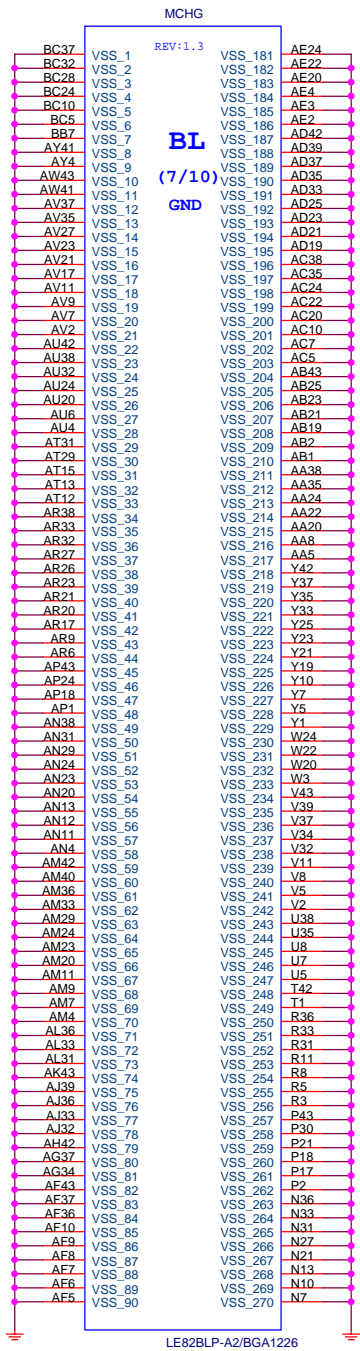
DMI:12/4/8/4/12
Impedance=95 +- 17.5%



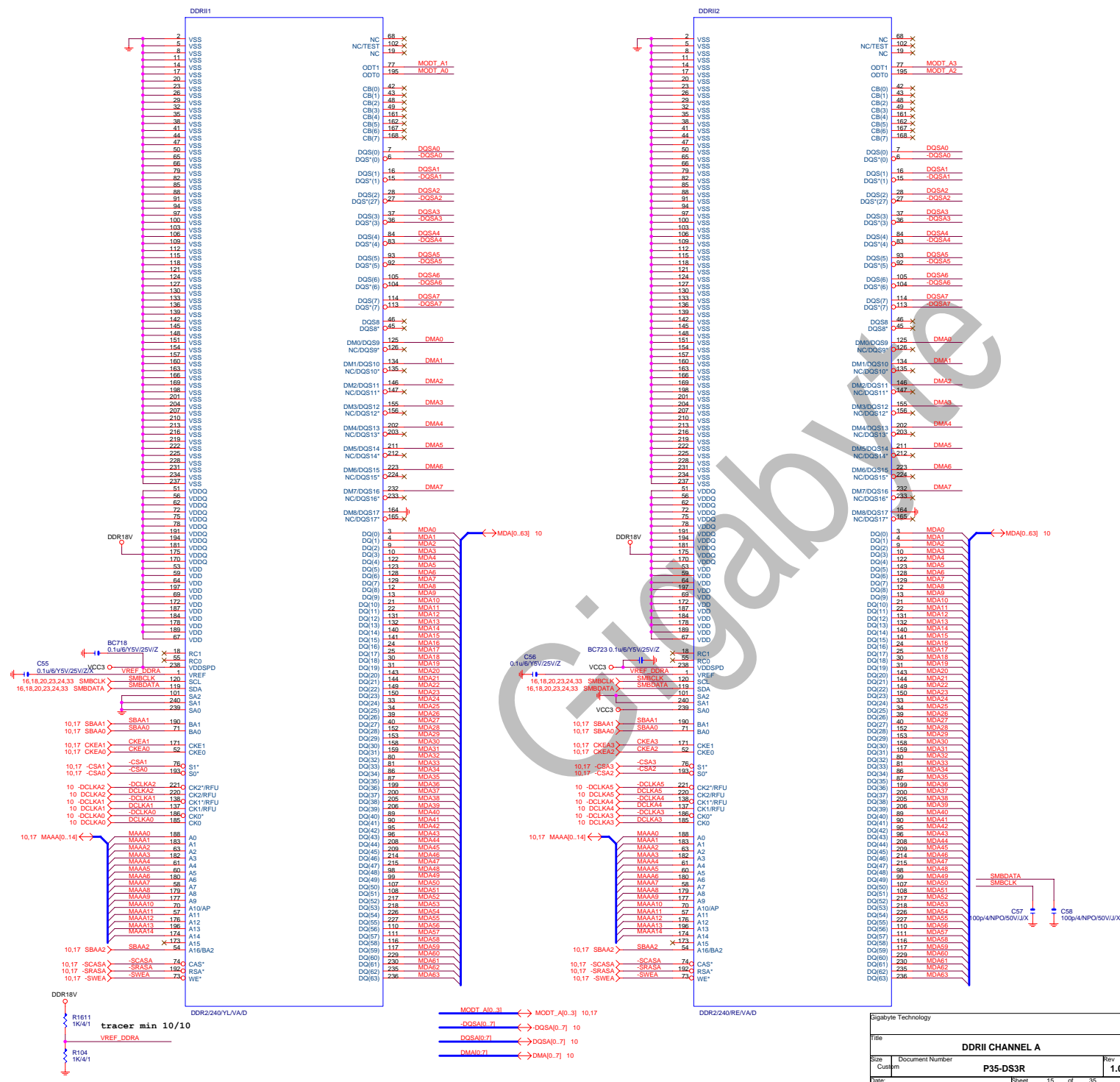
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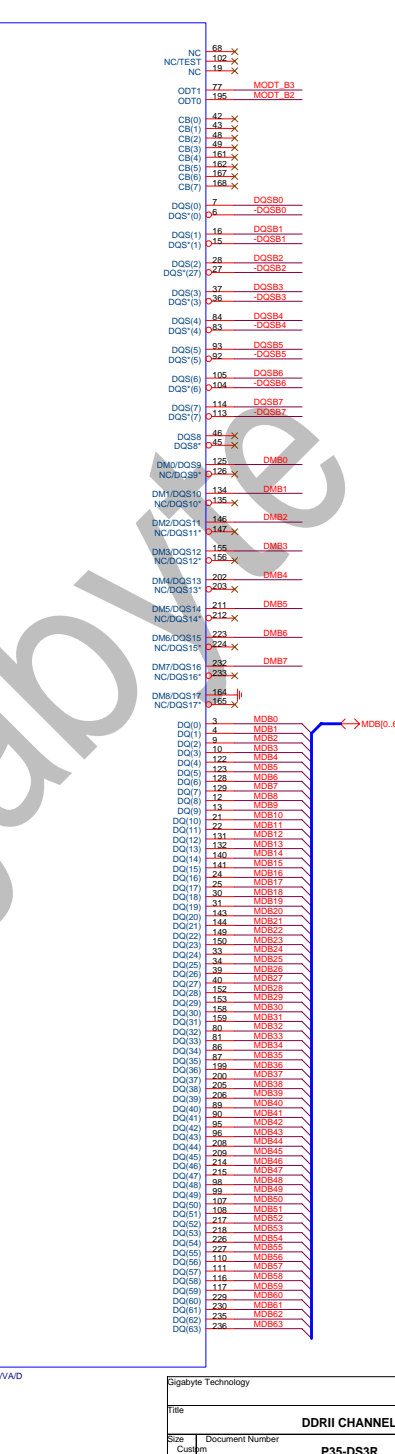
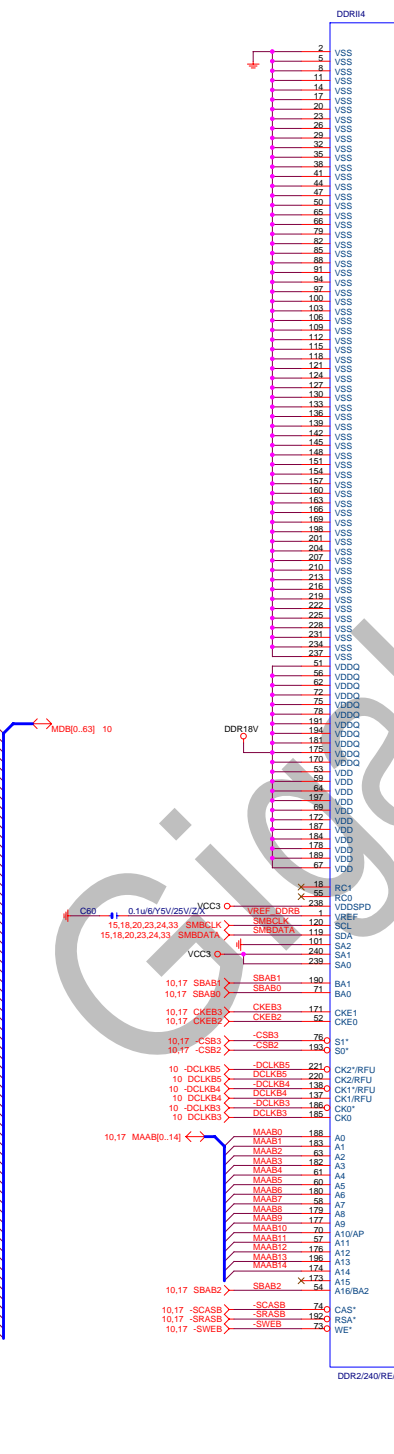
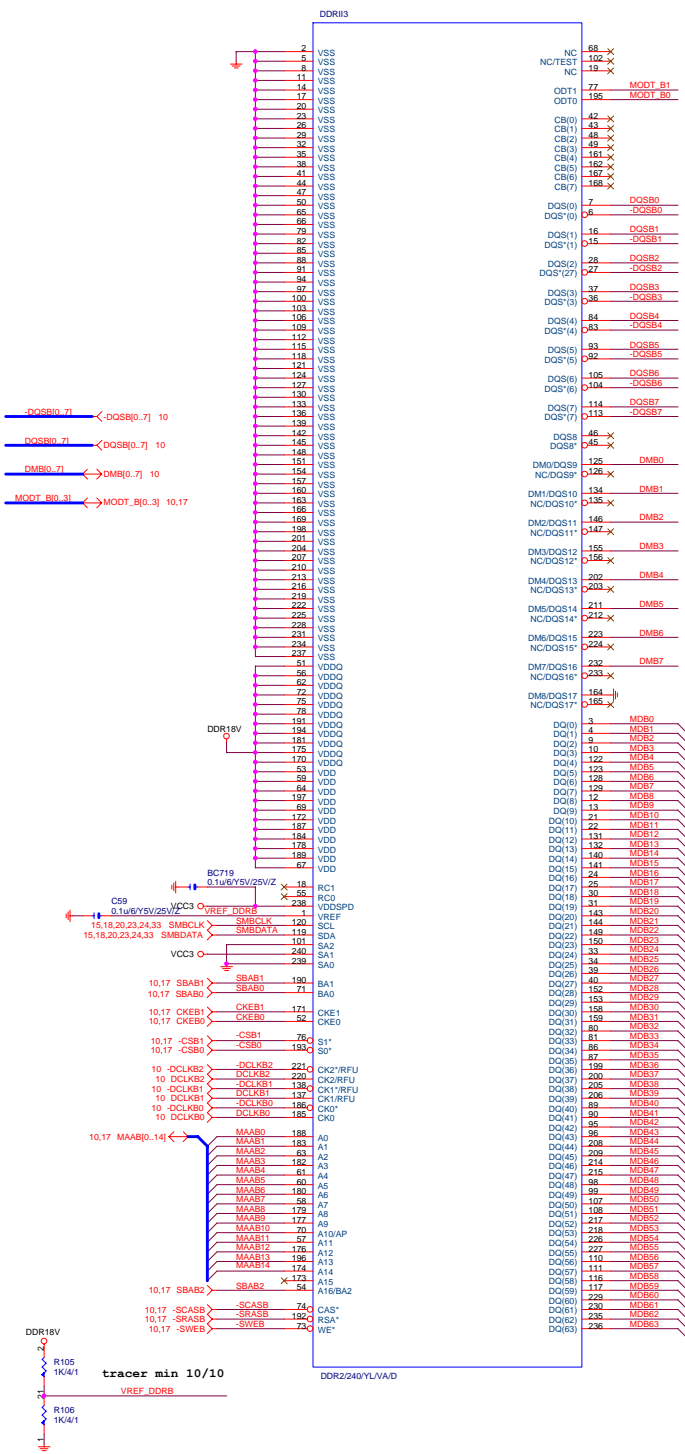
Title			
GMCH-PCI E & DMI			
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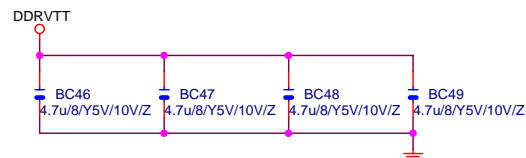
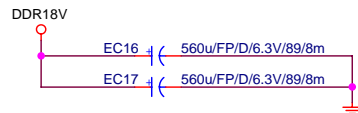
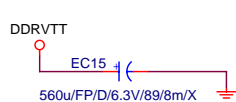






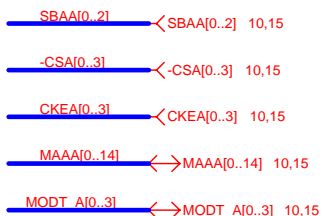
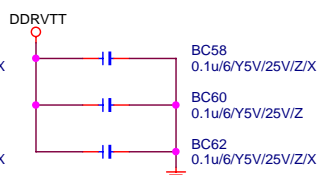
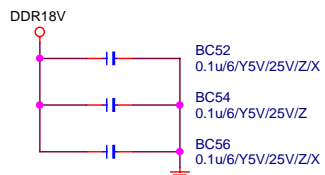
DDR TERMINATION CHANNEL A

DDRVTT Decouple



DDR18V Decouple

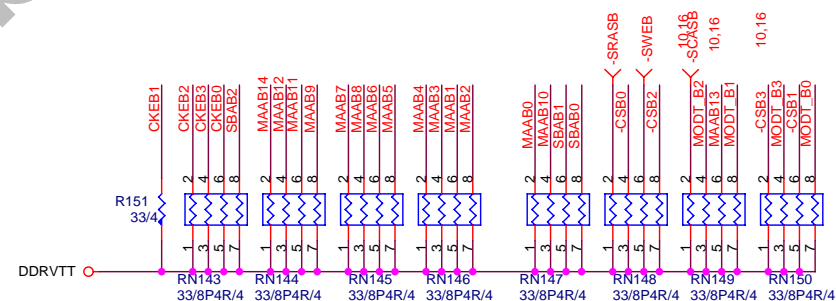
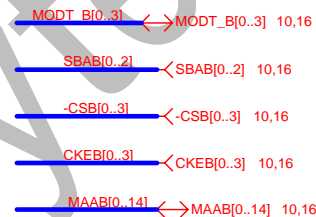
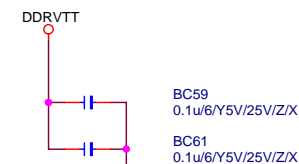
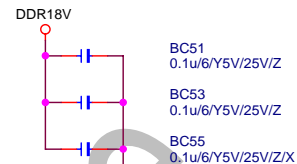
DDRVTT Decouple



DDR TERMINATION CHANNEL B

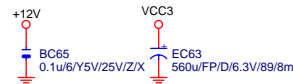
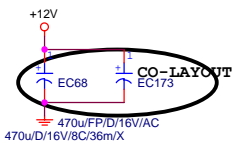
DDR18V Decouple

DDRVTT Decouple

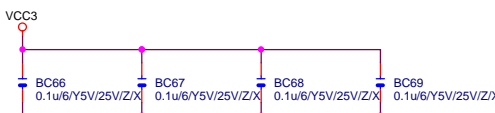


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Title			DDRII TERMINATOR
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PCIESLOT-164DN-2 3GIO_*16



15,16,20,23,24,33 SMBCLK
15,16,20,23,24,33 SMBDATA

20,24,34 -PCIE_WAKE

PCIEX16:15/4/8/4/15

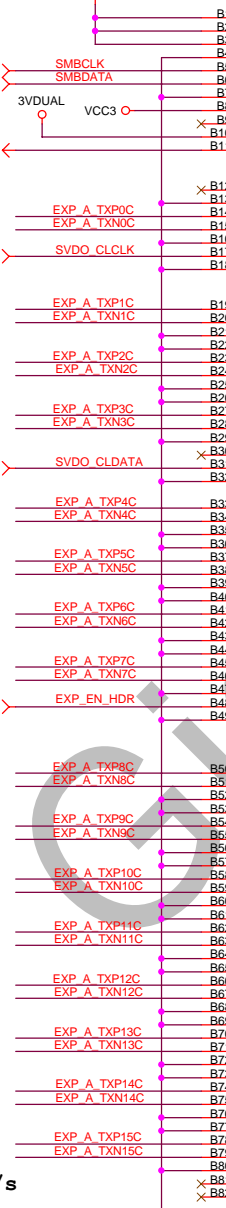
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EXP A TXN[0..15] >>> EXP_A_TXN[0..15] 11

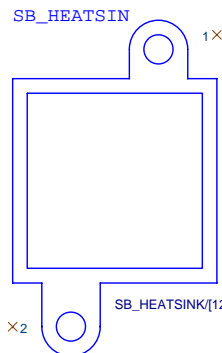
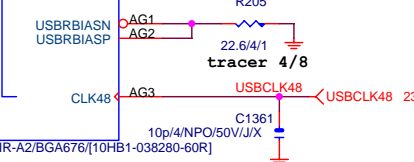
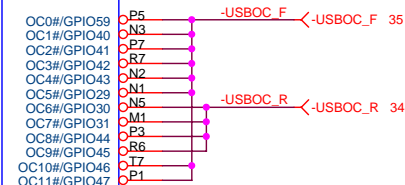
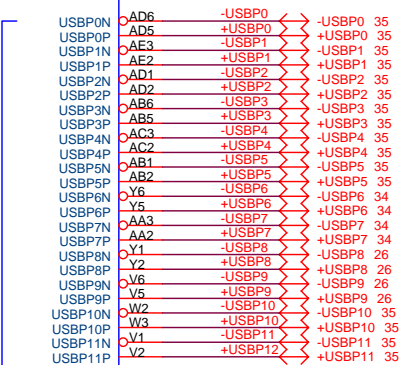
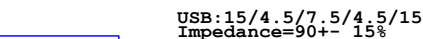
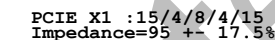
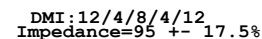
EXP A TXP0	C92	0.1u4/Y5V/16V/Z	EXP A TXP0C
EXP A TXN0	C93	0.1u4/Y5V/16V/Z	EXP A TXN0C
EXP A TXP1	C94	0.1u4/Y5V/16V/Z	EXP A TXP1C
EXP A TXN1	C95	0.1u4/Y5V/16V/Z	EXP A TXN1C
EXP A TXP2	C96	0.1u4/Y5V/16V/Z	EXP A TXP2C
EXP A TXN2	C97	0.1u4/Y5V/16V/Z	EXP A TXN2C
EXP A TXP3	C98	0.1u4/Y5V/16V/Z	EXP A TXP3C
EXP A TXN3	C99	0.1u4/Y5V/16V/Z	EXP A TXN3C
EXP A TXP4	C100	0.1u4/Y5V/16V/Z	EXP A TXP4C
EXP A TXN4	C101	0.1u4/Y5V/16V/Z	EXP A TXN4C
EXP A TXP5	C102	0.1u4/Y5V/16V/Z	EXP A TXP5C
EXP A TXN5	C103	0.1u4/Y5V/16V/Z	EXP A TXN5C
EXP A TXP6	C104	0.1u4/Y5V/16V/Z	EXP A TXP6C
EXP A TXN6	C105	0.1u4/Y5V/16V/Z	EXP A TXN6C
EXP A TXP7	C106	0.1u4/Y5V/16V/Z	EXP A TXP7C
EXP A TXN7	C107	0.1u4/Y5V/16V/Z	EXP A TXN7C
EXP A TXP8	C108	0.1u4/Y5V/16V/Z	EXP A TXP8C
EXP A TXN8	C109	0.1u4/Y5V/16V/Z	EXP A TXN8C
EXP A TXP9	C110	0.1u4/Y5V/16V/Z	EXP A TXP9C
EXP A TXN9	C111	0.1u4/Y5V/16V/Z	EXP A TXN9C
EXP A TXP10	C112	0.1u4/Y5V/16V/Z	EXP A TXP10C
EXP A TXN10	C113	0.1u4/Y5V/16V/Z	EXP A TXN10C
EXP A TXP11	C114	0.1u4/Y5V/16V/Z	EXP A TXP11C
EXP A TXN11	C115	0.1u4/Y5V/16V/Z	EXP A TXN11C
EXP A TXP12	C116	0.1u4/Y5V/16V/Z	EXP A TXP12C
EXP A TXN12	C117	0.1u4/Y5V/16V/Z	EXP A TXN12C
EXP A TXP13	C118	0.1u4/Y5V/16V/Z	EXP A TXP13C
EXP A TXN13	C119	0.1u4/Y5V/16V/Z	EXP A TXN13C
EXP A TXP14	C120	0.1u4/Y5V/16V/Z	EXP A TXP14C
EXP A TXN14	C121	0.1u4/Y5V/16V/Z	EXP A TXN14C
EXP A TXP15	C122	0.1u4/Y5V/16V/Z	EXP A TXP15C
EXP A TXN15	C123	0.1u4/Y5V/16V/Z	EXP A TXN15C

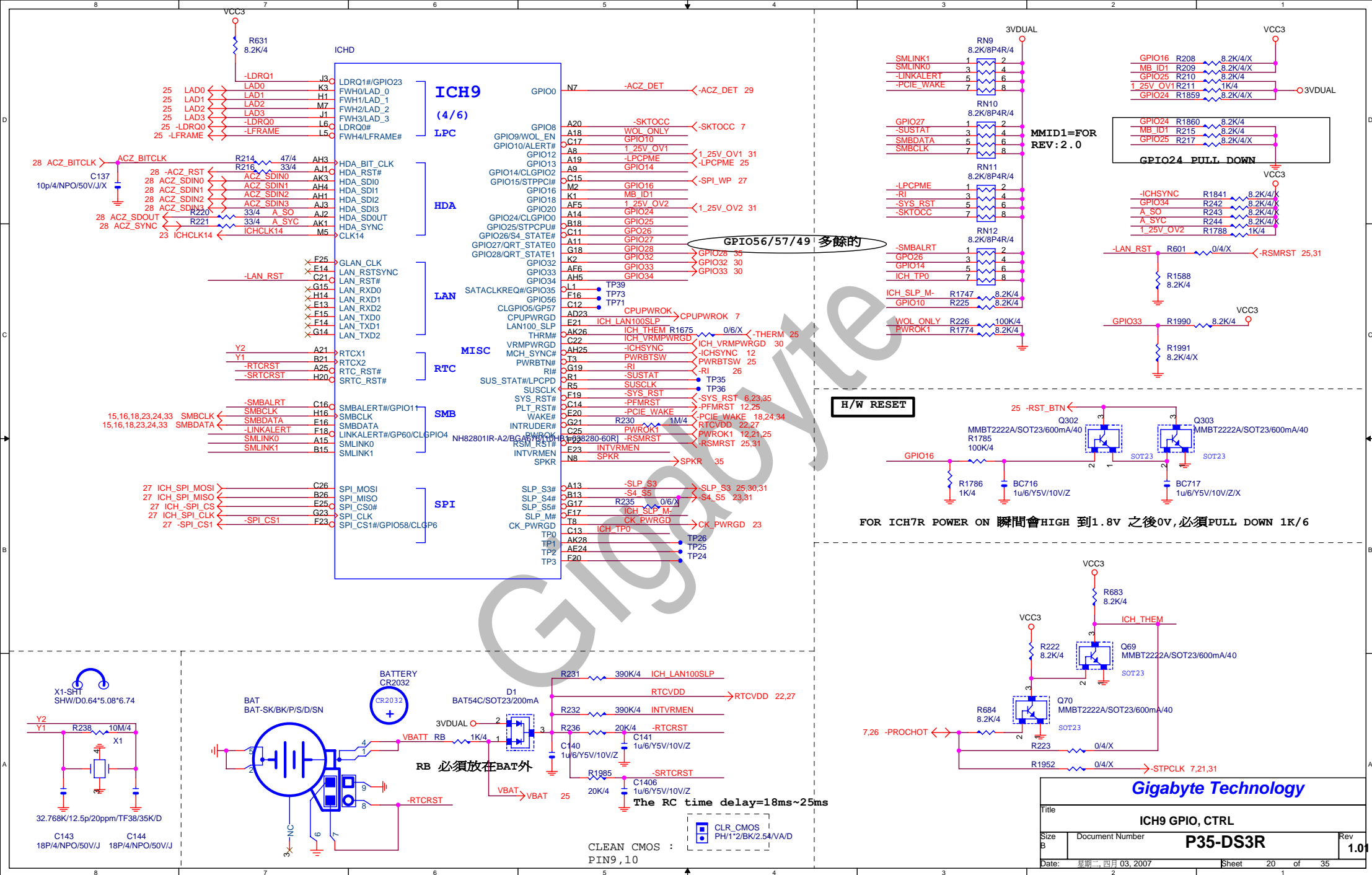
11 SDVO_CLKLK

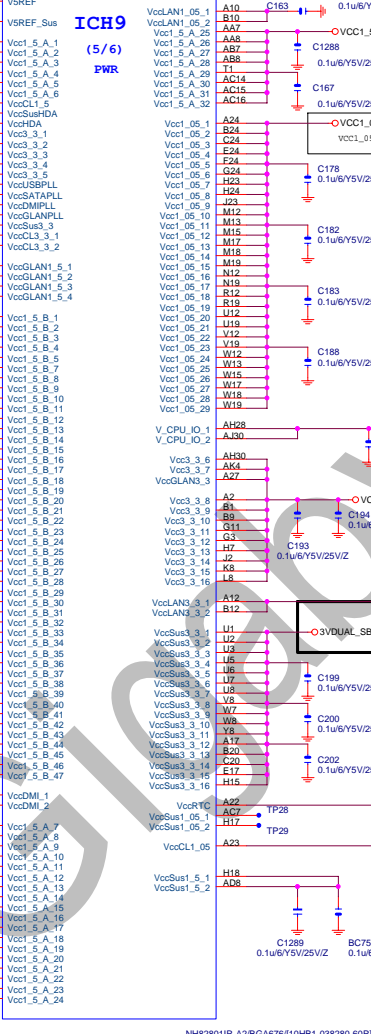
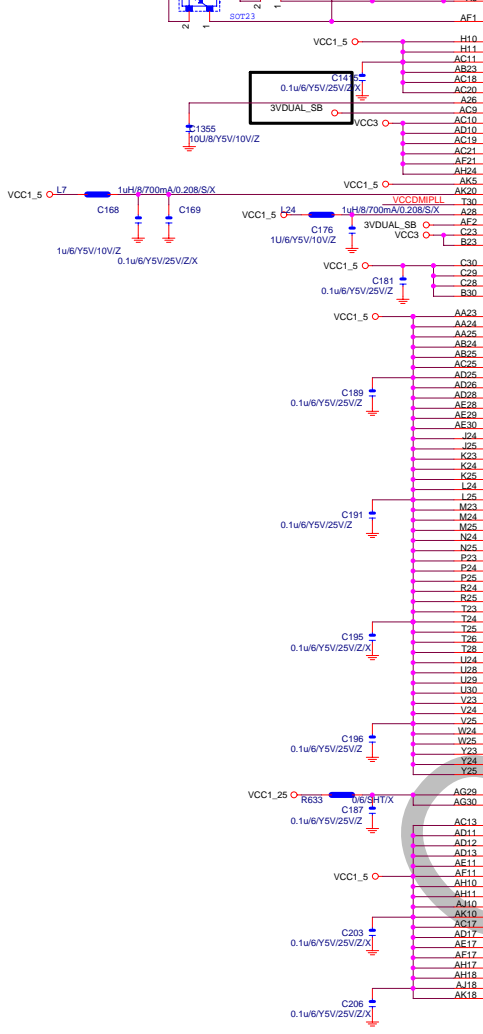
11 SDVO_CLDATA

12 EXP_EN_HDR





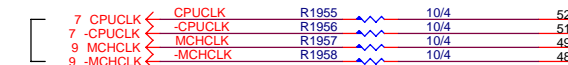




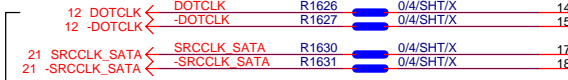
Gigabyte Technology			
File ICH9-PWR & GND			
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CLK GEN CK505

50欧姆: [18/4/10/4/18]



50欧姆: [18/4/10/4/18]



50欧姆: [18/4/10/4/18]

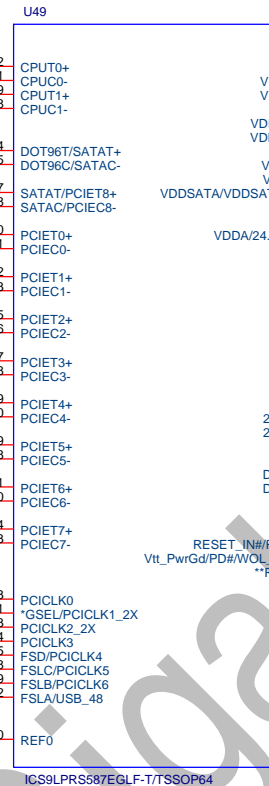


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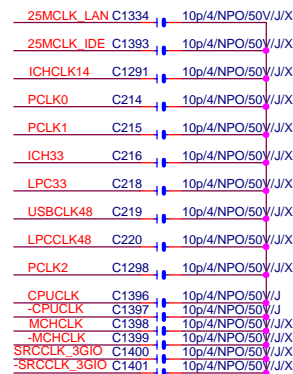
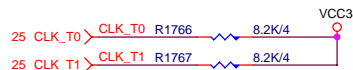
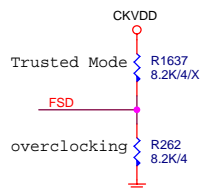
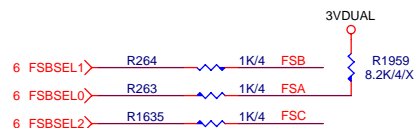
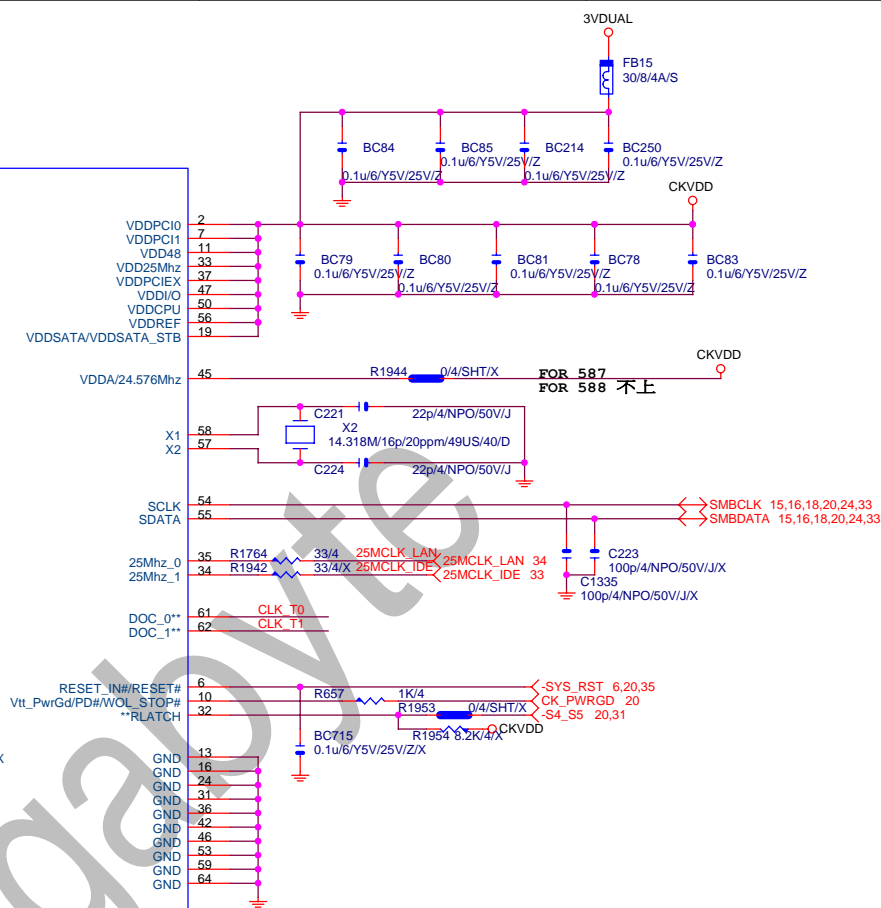


50欧姆: [4/10]

50欧姆: [4/10]



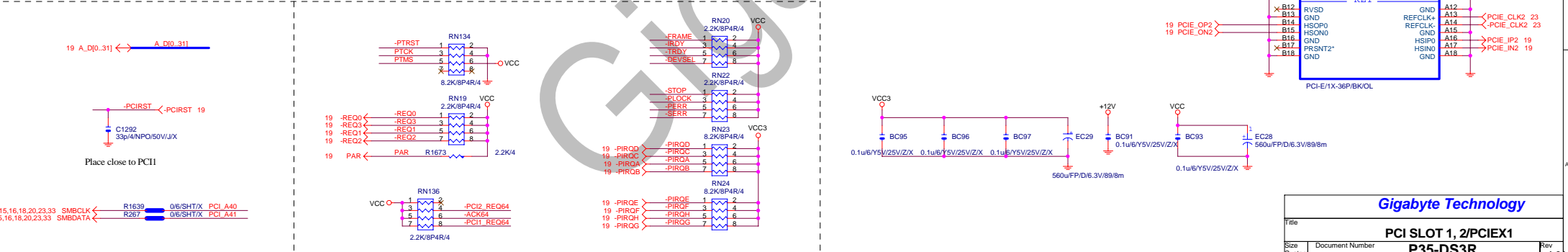
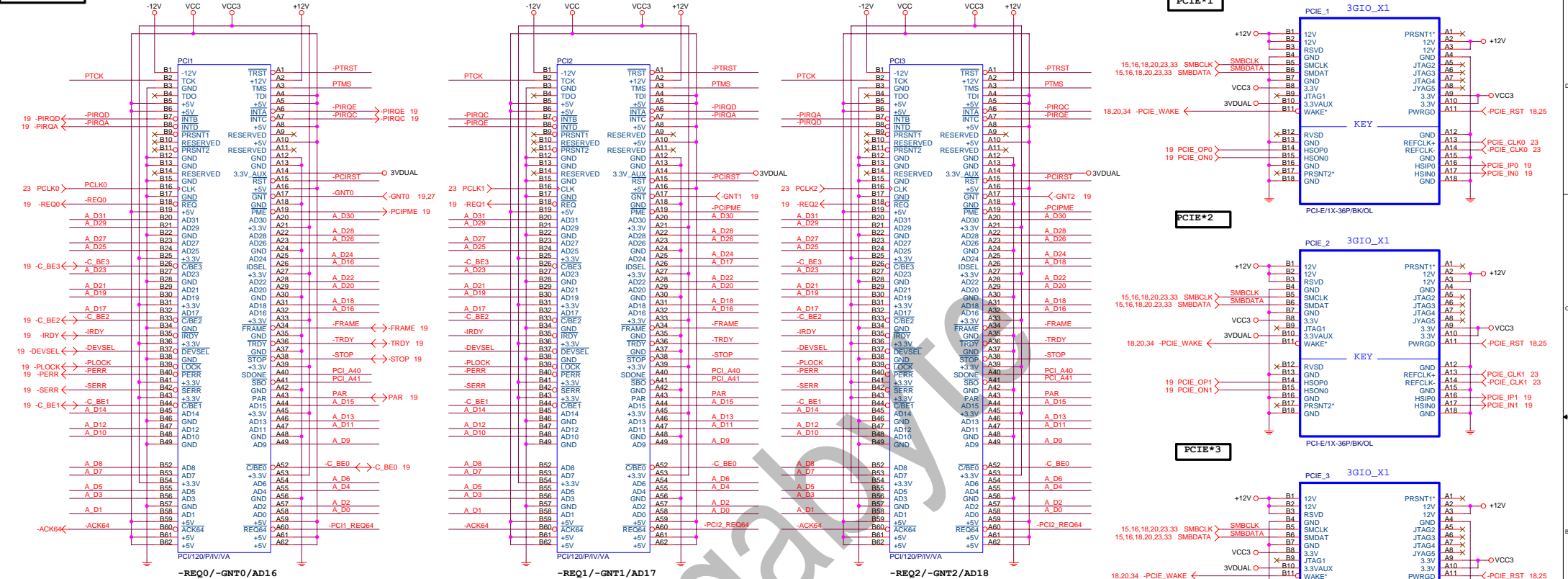
GSEL=1,96Mhz from 14/15,SATACLK from 17/18
GSEL=0,SATACLK from 14/15,PCIECLK from 17/18



Gigabyte Technology

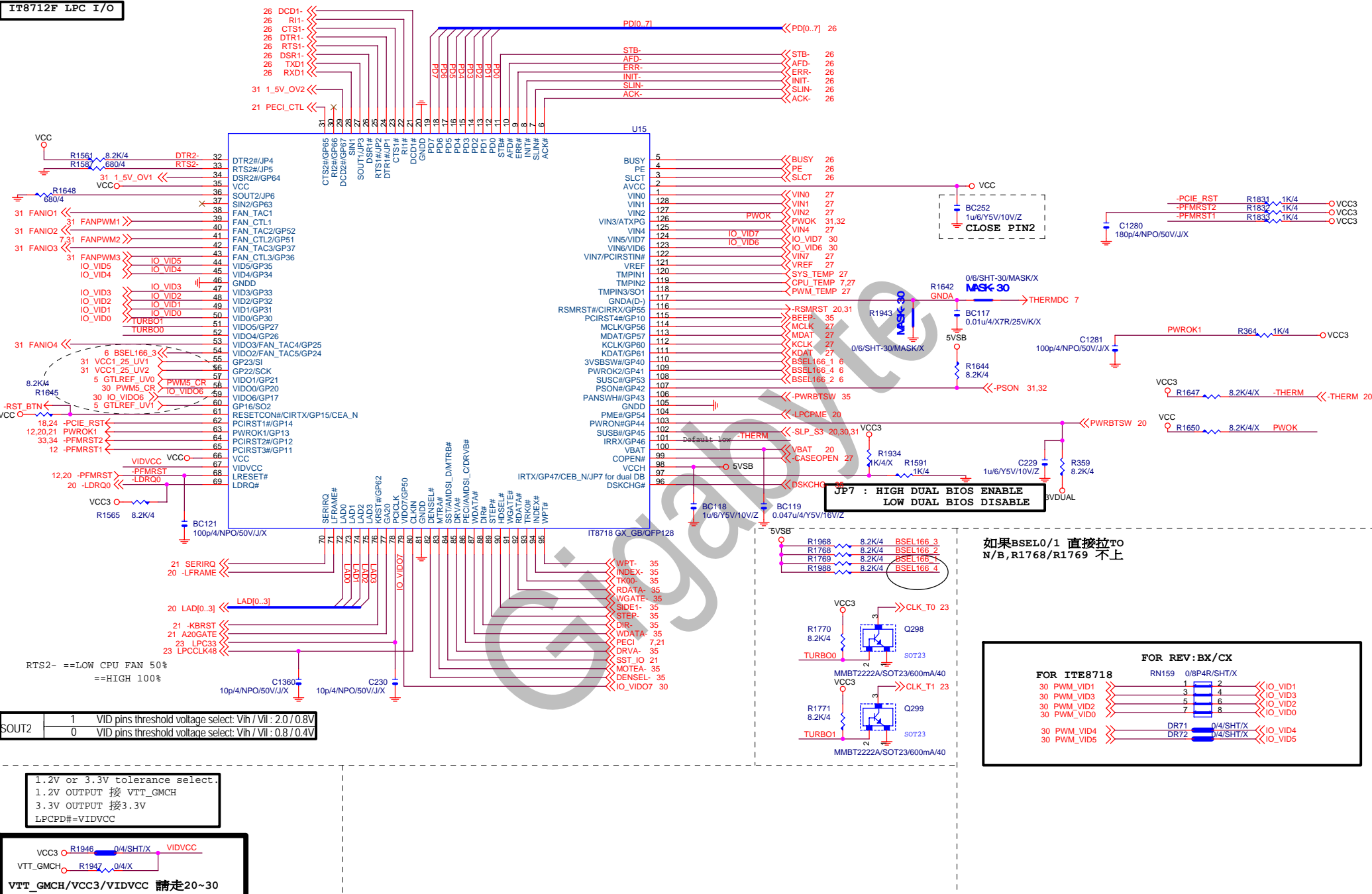
Title			
CK505 CLK GEN			
P35-DS3R			
Size	Document Number	Rev	1.0
Custom			
Date:	星期三, 四月 03, 2007	Sheet	23 of 35

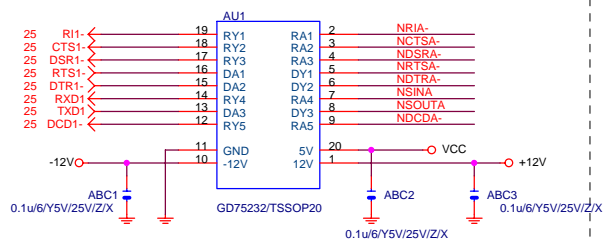
PCI1,2 SLOT



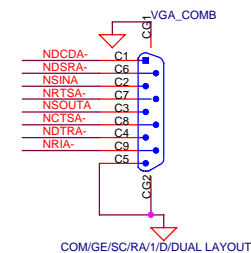
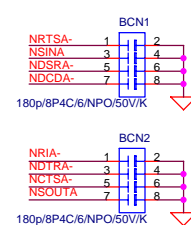
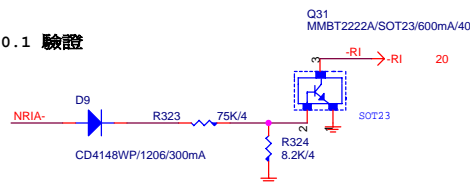
Gigabyte Technology			
Title	PCI SLOT 1, 2/PCIEX1		
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IT8712F LPC I/O

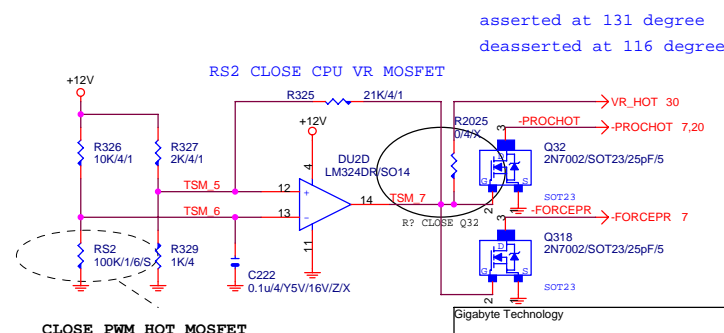
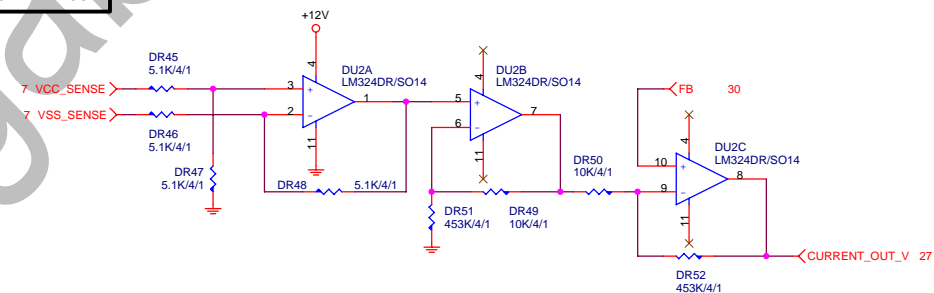
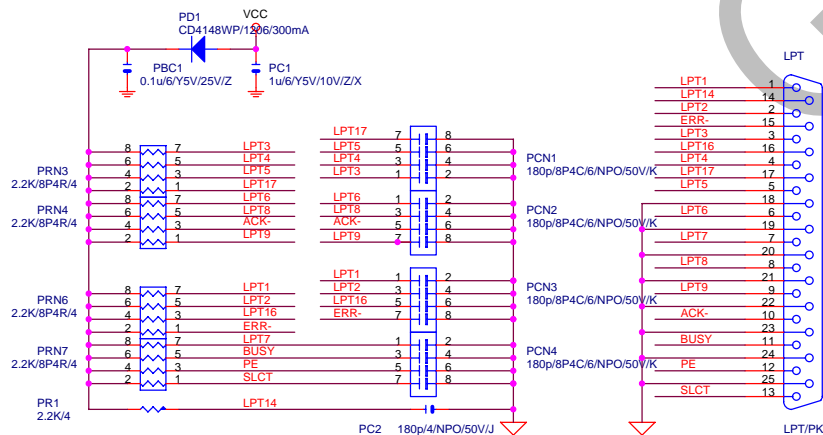
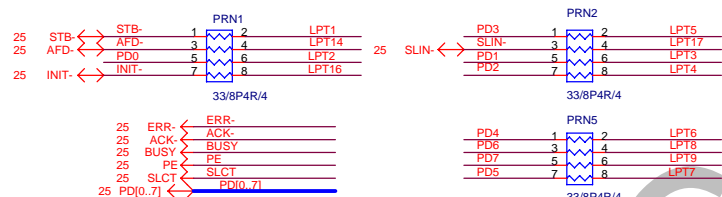
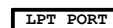
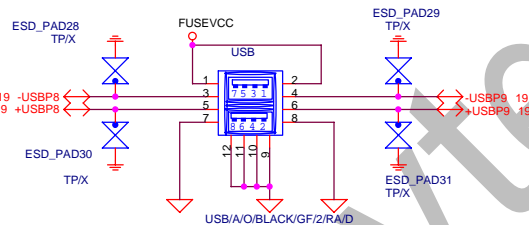
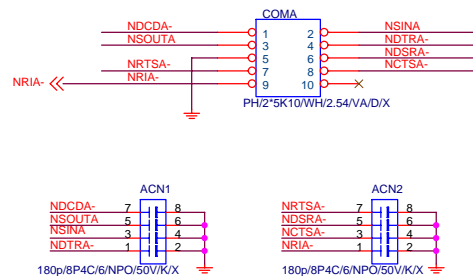




REV:0.1 驗證



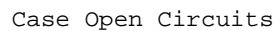
PLACE NEAR VGA COM CONNECTOR



TEMP H/W MONITOR



CASE OPEN



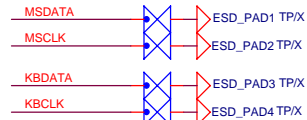
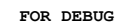
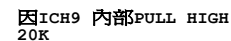
VOLTAGE-- H/W MONITOR

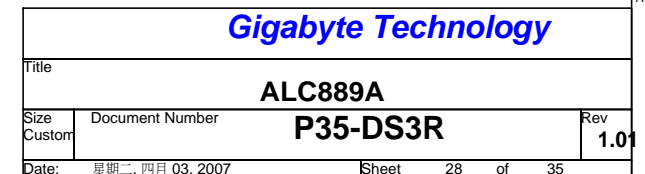


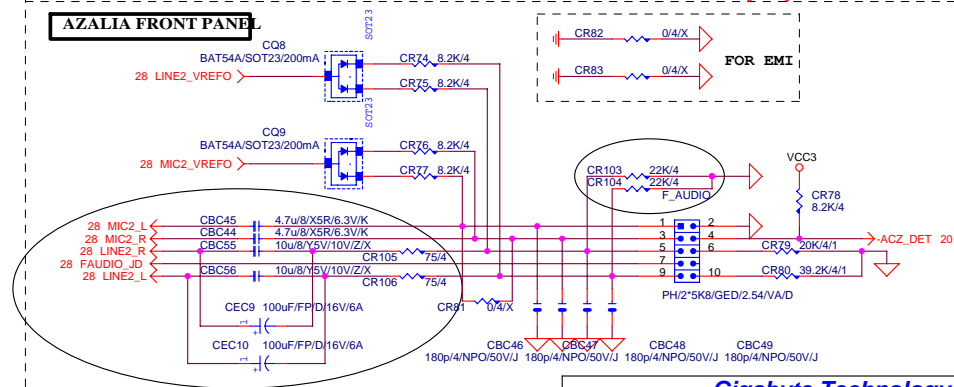
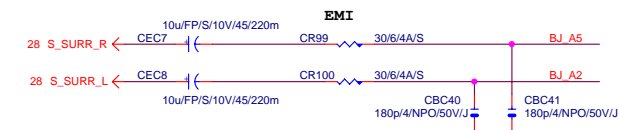
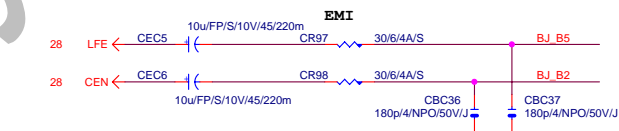
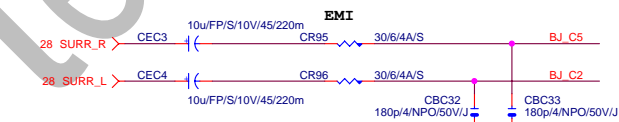
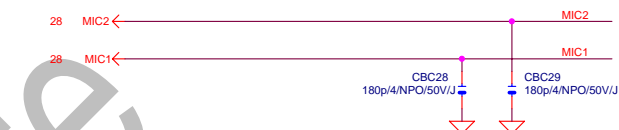
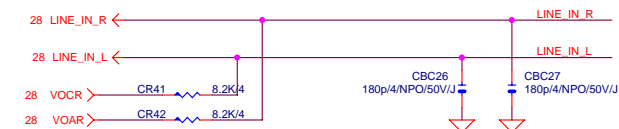
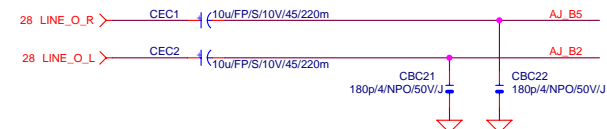
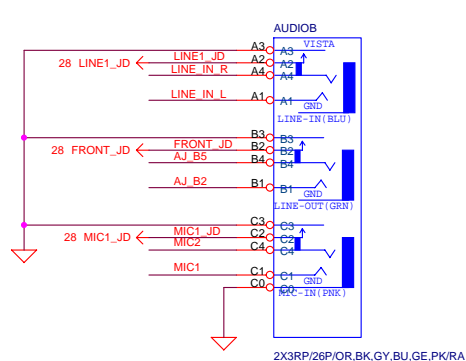
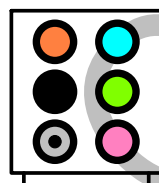
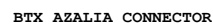
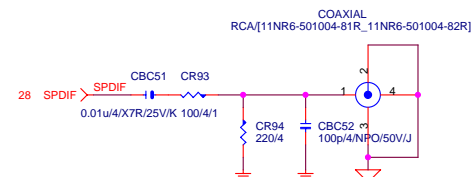
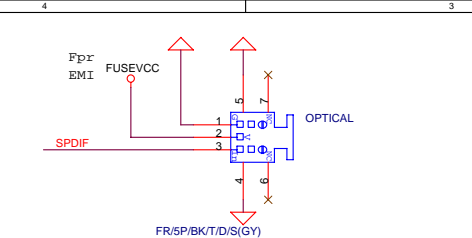
KB/MS



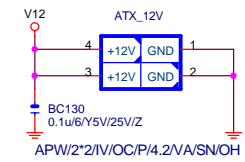
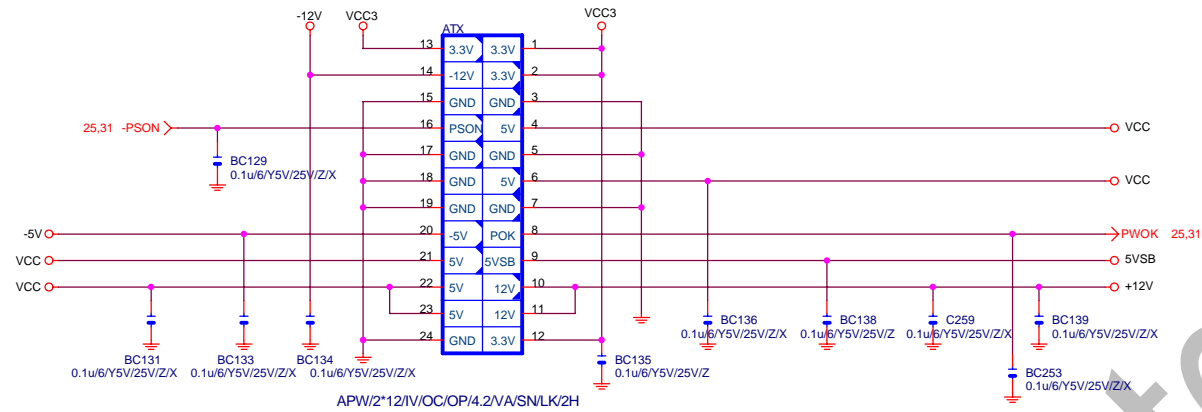
BOOT DEVICE	GNT0	CS1
SPI	0	1
PCI	1	0
FWH	1	1



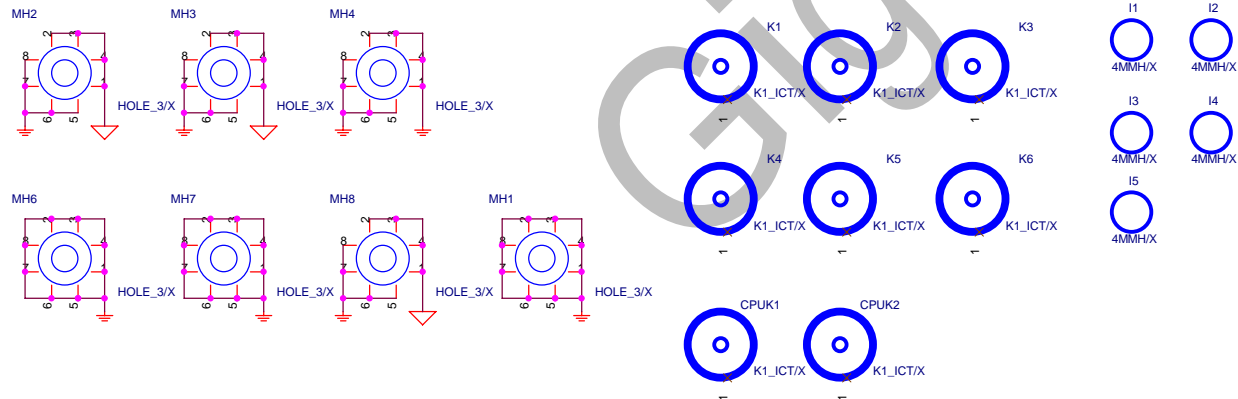




ATX POWER CONNECTOR



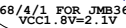
HOLE_3-2--->有鉛



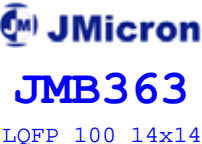
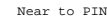
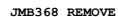
Gigabyte Technology

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ATX POWER CONNECTOR			
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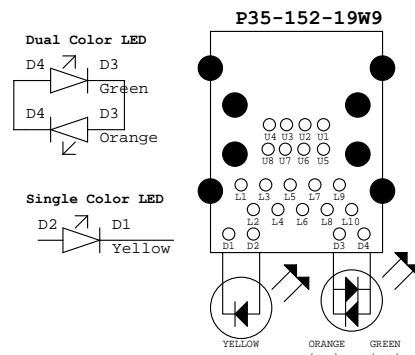
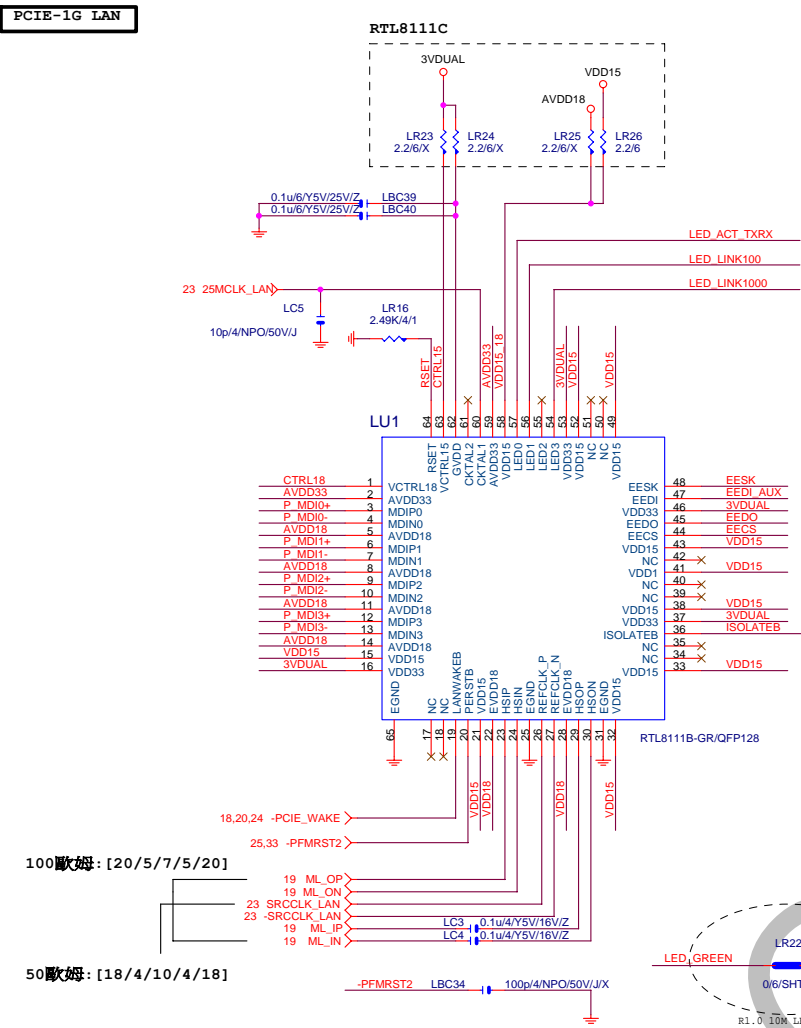
APL1117/SOT223/0.8A



Impedance=95 \pm 17.5%

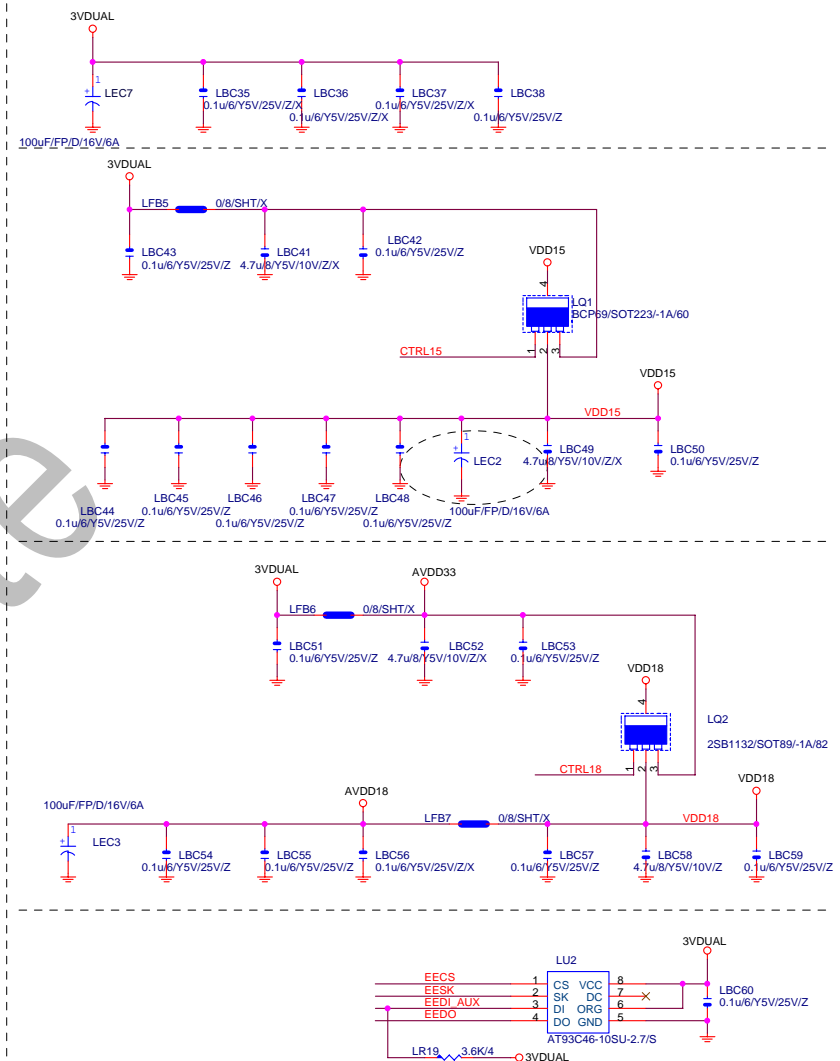


PCIE-1G LAN

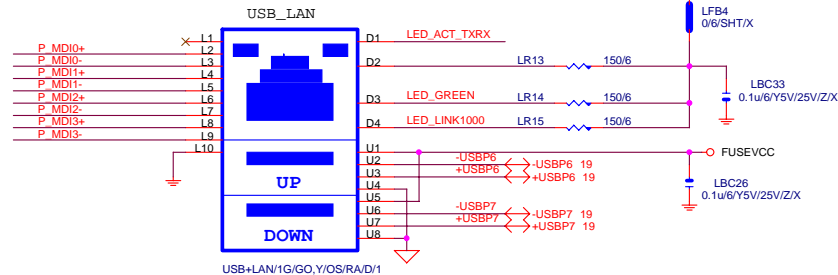
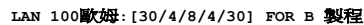


Power domain chart

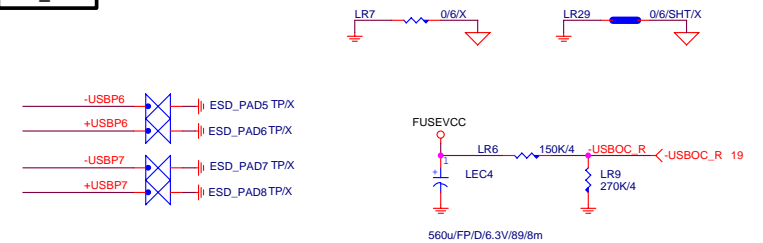
	RTL8111B / RTL8101E	RTL8111C
AVDD33	3.3V	3.3V
AVDD18	1.8V	1.2V
EVDD18	1.8V	1.2V
DVDD15	1.5V	1.2V



USB_LAN_CONNECTOR



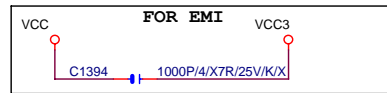
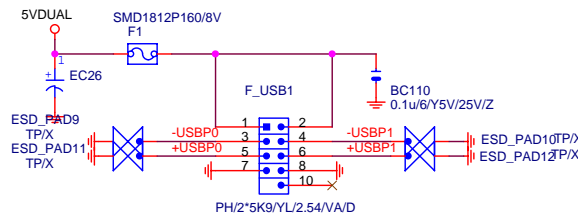
USB LAN



FRONT USB1

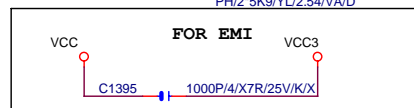
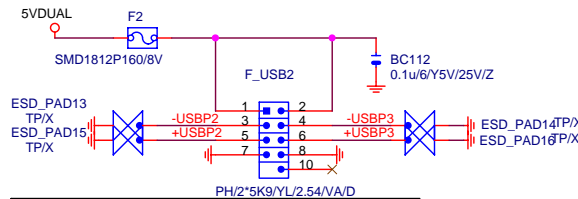
19 +USBP0 <-> +USBP0
19 -USBP0 <-> -USBP0
19 +USBP1 <-> +USBP1
19 -USBP1 <-> -USBP1

560u/FP/D/6.3V/89/8m



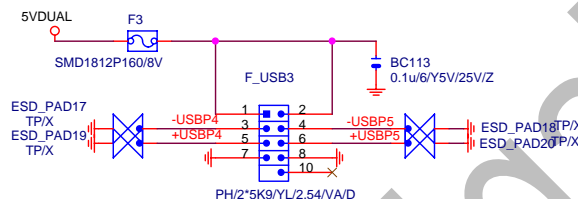
FRONT USB2

19 +USBP2 <-> +USBP2
19 -USBP2 <-> -USBP2
19 +USBP3 <-> +USBP3
19 -USBP3 <-> -USBP3



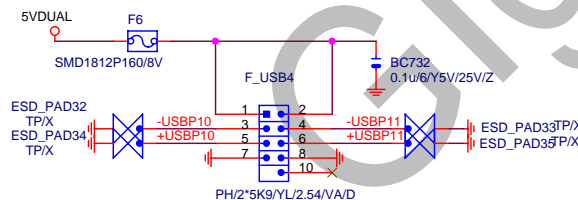
FRONT USB3

19 -USBP4 <-> -USBP4
19 +USBP4 <-> +USBP4
19 -USBP5 <-> -USBP5
19 +USBP5 <-> +USBP5

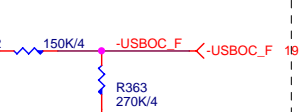


FRONT USB4

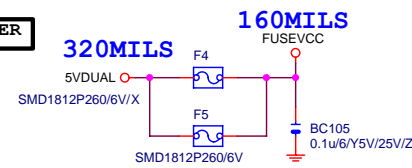
19 -USBP10 <-> -USBP10
19 +USBP10 <-> +USBP10
19 -USBP11 <-> -USBP11
19 +USBP11 <-> +USBP11



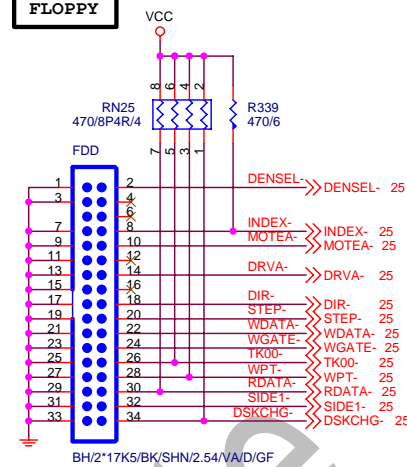
FRONT USB OC1



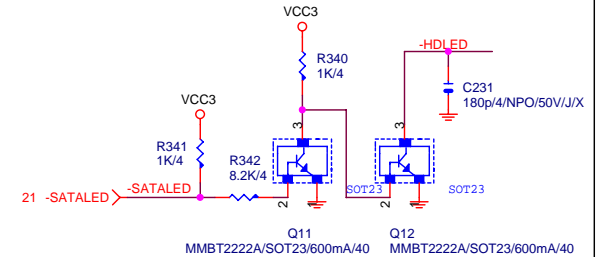
USB POWER



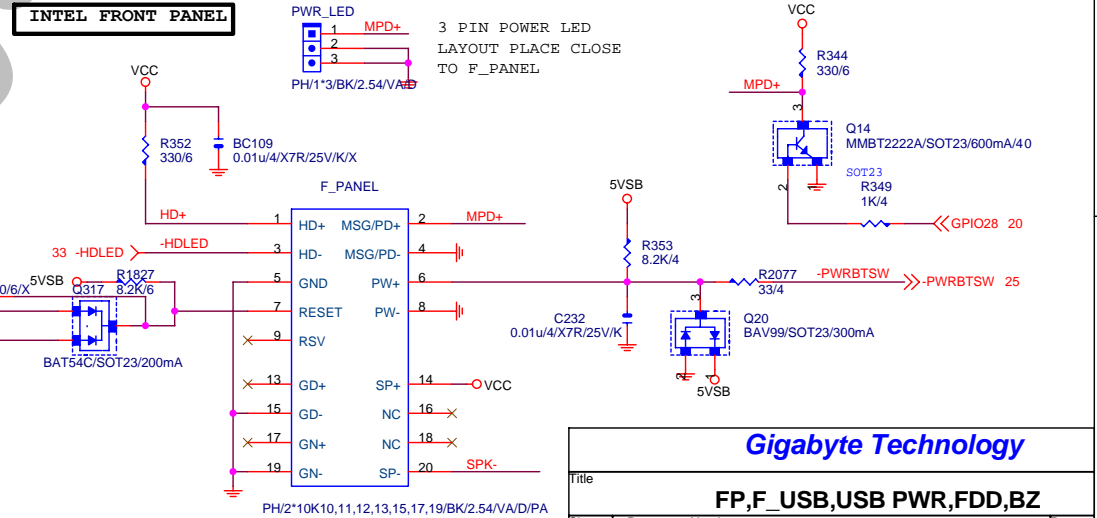
FLOPPY



SATA LED



INTEL FRONT PANEL



Gigabyte Technology

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